IES-2010- Paper-II

1.   Consider the following statements:

When compared with a bridge rectifier, a centre-trapped full wave rectifier,

(1) Has larger transformer utilization factor

(2) Can be used for floating output terminals i.e. no input terminal is grounded.

(3) Need two diodes instead of four

(4) Needs diodes of a lower PIV rating.

Which of the above statements are correct?

(A) 1 and 2 only   (B) 1, 2, 3 and 4

(C) 3 only   (D) 3 and 4 only

2.   A linear time-invariant system initially at rest, when subjected to a unit-step input, gives a response $y(t) = te^{-t}, t > 0$ the transfer function of the system is:

(A) $\dfrac{1}{(s+1)^2}$ (B) $\dfrac{1}{s(s+1)^2}$ (C) $\dfrac{s}{(s+1)^2}$ (D) $\dfrac{1}{(s+1)}$

3.   The hexadecimal representation of 657_8 is:

(A) 1AFH   (B) D78H   (C) D71H   (D) 32FH

4.   A 13 bit PCM system performance is better than an 8 bit PCM system because:

(A) Noise is lower and is proportional to reciprocal of bandwidth

(B) Bandwidth is larger and detection is easier

(C) Quantization noise is lesser other things being equal

(D) Quantization noise is nearly zero since 2^{13} levels are available

5.   Match List – I with List – II and select the correct answer using the code given below the lists:

List – I (Device)	**List – I (Application)**
(a) Transferred electron devices	(1) Microwave amplifier
(b) Two-cavity klystron	(2) Microwave oscillator
(c) PIN diode	(3) Microwave low power measurement
(d) Bolometer	(4) Modulator

Code:

	a	b	c	d
(A)	3	1	4	2
(B)	2	1	4	3
(C)	3	4	1	2
(D)	2	4	1	3

6.   The default parameter-passing mechanism in C-programming language is:
      (A) Call by reference   (B) Call as random
      (C) Call by value   (D) Call by value result

7.   Consider the function $F(s) = \dfrac{5}{s(s^2 + 3s + 2)}$, where F(s) is Laplace transform of function f(t).

      The initial value of f(t) is:
      (A) 7   (B) 5/2   (C) 5/3   (D) 0

8.   The data type defined by user is:
      (A) Built-in data type   (B) Abstract data type
      (C) Homogeneous data type   (D) Real data type

9.   Insertion of a record in a circularly linked list involves the modification of:
      (A) 4 pointers   (B) 3 pointers   (C) 2 pointers   (D) 1 pointers

10.   The spectrum of a band pass signal spans from 20kHz to 30kHz. The signal can be recovered ideally from the sampled values when the sampling rate is at least:
      (A) 20kHz   (B) 60kHz   (C) 50kHz   (D) 40kHz

11.   Match List-I with List –II and select the correct answer using the code given below the lists:
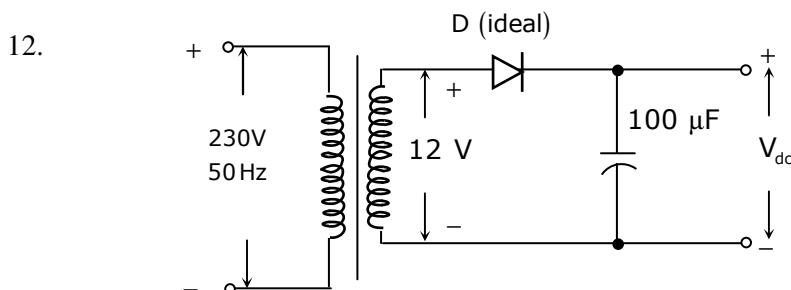
      List – I   **List – II**

      (a) Rectangular waveguide   (1) Plane of polarization

      (b) Waveguide twists   (2) Waveguide tuner

      (c) Slotted section   (3) TE_{10} mode

      (d) Stub screws   (4) VSWR measurement

Code:

	a	b	c	d
(A)	2	4	1	3
(B)	3	4	1	2
(C)	2	1	4	3
(D)	3	1	4	2

12.

The output V_{dc} from the above circuit is:

(A) $12\sqrt{2}$ (B) $12/\pi$ (C) $24/\pi$ (D) $12/\sqrt{2}$

13. Which of the following data structure is used by a compiler to manage information about variables and their attributes?

(A) Abstract syntax tree   (B) Linked list

(C) Parse table   (D) Symbol table

14. A transistor works in three regions:

(1) Cut-off

(2) Active

(3) Saturation

While used a switch in digital logic gates, the regions it works in are:

(A) 1 and 2 only   (B) 2 and 3 only

(C) 1 and 3 only   (D) 1, 2 and 3

15. The number of edges in a regular graph of degree D and N vertices is equal to:

(A) ND   (B) ND/2   (C) N + D   (D) N^D

16. The Boolean expression $\overline{A+\overline{B}+C}+\overline{\overline{A}+\overline{B}+C}+\overline{A+\overline{B}+\overline{C}}+ABC$ reduces to:

(A) A   (B) B   (C) C   (D) A+B+C

17. A single bus structure is primarily found in:

(A) Main frames   (B) Mini and micro computers

(C) Super computers   (D) High performance machines

18. In a unity feedback control system with $G(s) = \dfrac{4}{s^2 + 0.4s}$ when subjected to unit step input, it is required that system response should be settled within 2% tolerance band; the system settling time is:

(A) 1sec   (B) 2sec   (C) 10sec   (D) 20sec

19. The ability to halt the CPU temporarily and use this interval of time to send information on buses is called:

(A) Cycle stealing   (B) Vectoring an interrupt

(C) Polling   (D) Direct memory access

20. A rectifier (without filter) with fundamental ripple frequency equal to twice the mains frequency, has ripple factor of 0.482 and power conversion efficiency equal to 81.2%. The rectifier is:

   (1) Bridge rectifier

   (2) Full-wave (non bridge) rectifier

   (3) Half-wave rectifier.

   Which of these are correct?

   (A) 2 and 3 only   (B) 2 only   (C) 1 and 2 only   (D) 1, 2 and 3

21. Consider the following:

   (1) Compliers

   (2) Designs

   (3) Evaluation

   (4) Instruction set architecture

   Which of these are included in the present definition of computer architecture to design a full computer system?

   (A) 1, 2 and 3   (B) 1, 3 and 4

   (C) 2, 3 and 4   (D) 1, 2, 3 and 4

22. The Nyquist rate for the signal $x(t) = 2\cos(2000\,\pi t)\cos(5000\,\pi t)$, is:

   (A) 7kHz   (B) 5kHz   (C) 14kHz   (D) 10kHz

23. Given the main memory access time is 1200ns and Cache access time is 100ns; the average memory access time is not to exceed 120ns. The hit ratio for the Cache must be at least:
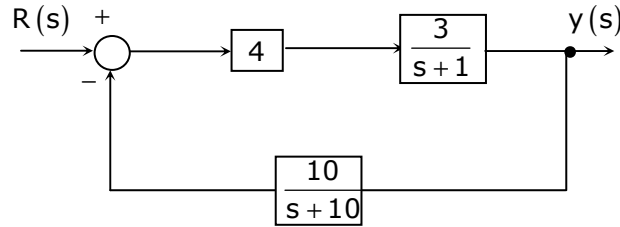
   (A) 90%   (B) 98%   (C) 80%   (D) 75%

24.
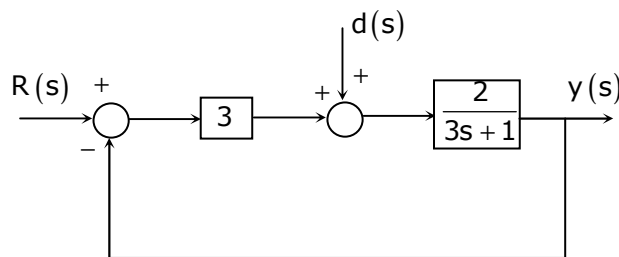
   The transfer function from d(s) to y(s) is:

   (A) $\dfrac{2}{3s+7}$ (B) $\dfrac{2}{3s+1}$ (C) $\dfrac{6}{3s+7}$ (D) $\dfrac{2}{3s+6}$

25. An I/O processor controls the flow of information between:

   (A) Cache memory and I/O devices   (B) Main memory and I/O devices

   (C) Two I/O devices   (D) Cache and main memory

26.

The characteristic equation of the above closed-loop system is:

(A) $s^2 + 11s + 10 = 0$

(B) $s^2 + 11s + 130 = 0$

(C) $s^2 + 10s + 120 = 0$

(D) $s^2 + 10s + 12 = 0$

27. The standard SOP expression for Boolean expression $A\bar{B} + AC + BC$ is:

(A) $A\bar{B}C + \bar{A}BC + A\bar{B}\bar{C} + ABC$

(B) $AB\bar{C} + \bar{A}BC + ABC$

(C) $A\bar{B}C + AB\bar{C} + ABC$

(D) $\overline{AB}C + A B\bar{C} + A\overline{BC}$

28. The complement of the expression $Y = ABC + AB\bar{C} + \overline{AB}C + \bar{A}BC$ is

(A) $(A + \bar{B})(A + \bar{C})$

(B) $(\bar{A} + B)(A + C)$

(C) $(A + \bar{B})(\bar{A} + C)$

(D) $(\bar{A} + \bar{B})(A + \bar{C})$

29. Which one of the following has the shortest access time?
   (A) NMOS EPROM
   (B) NMOS RAM
   (C) CMOS RAM
   (D) Bipolar static RAM

30. The main disadvantage of using coaxial cable for microwave signals is its:
   (A) Low selectivity
   (B) Low distortion
   (C) High attenuation
   (D) High sensitivity

31. If the gain of the system is reduced to zero value, the roots of the system in the s-plane
   (A) Coincide with zeros
   (B) Move away from the zeros
   (C) Move away from the poles
   (D) Coincide with the poles

32. The figure of merit of a logic family is given by the product of:
   (A) Gain and bandwidth
   (B) Propagation delay time and power dissipation
   (C) Fan-out and propagation delay time
   (D) Noise margin and power dissipation

33. In NOR-NOR configuration, the minimum number of NOR gates needed to implement the switching function $X + X\overline{Y} + X\overline{Y}Z$ is:

    (A) 5   (B) 3   (C) 2   (D) 0

34. The addition of open loop zero pulls the root-loci towards:

    (A) The left and therefore system becomes more stable

    (B) The right and therefore system becomes unstable

    (C) Imaginary axis and therefore system becomes marginally stable

    (D) The left and therefore system becomes unstable

35. Match List–I with List-II and select the correct answer using the code given below the lists:

List – I	**List – II**
(a) HTL	(1) High fan-out
(b) CMOS	(2) Highest speed of operation
(c) I²L	(3) High noise immunity
(d) ECL	(4) Lowest product of power and delay

    Codes:

	a	b	c	d
(A)	3	4	1	2
(B)	2	1	4	3
(C)	3	1	4	2
(D)	2	4	1	3

36. On receiving an interrupt from an I/O device, the CPU:

    (A) Halts for a predetermined time

    (B) Branches off to the interrupt service routine after completion of the current instruction

    (C) Branches off to the interrupt service routine immediately

    (D) Hands over control of address bus and data bus to the interrupting device

37. Consider the following statements with regard to a PLL:

    (1) The 'capture range' of a PLL primarily depends upon the slope of transition band edge of the low-pass filter

    (2) The 'lock range' is not affected by the slope of transition band of the low-pass filter.

    Which of the above statements is/are correct?

    (A) Both 1 and 2   (B) 1 only

    (C) 2 only   (D) Neither 1 nor 2

38.   In microprocessor based systems DMA facility is required to increase the speed of data transfer between the:

(A) Microprocessor and the I/O devices

(B) Microprocessor and the memory

(C) Memory and the I/O devices

(D) Memory and the reliability system

39.   If x(t) is of finite duration and is absolutely integrable, then the 'region of convergence' is:

(A) Entire s plane   (B) From $\sigma = -1$ to $\sigma = +\infty$

(C) From $\sigma = +1$ to $\sigma = -\infty$ (D) Entire right half plane

40.   The quantization noise of a PCM system depends on:

(A) Number of quantization levels

(B) Step-size

(C) Both step-size and number of quantization levels

(D) Sampling rate

41.   Match List – I with List – II and select the correct answer using the code given below the lists:

List – I	**List – II**
(a) Pipelined ALU	(1) RISC
(b) Simpler compiler	(2) CISC
(c) Separate data and instruction caches	(3) Mixed RISC-CISC
(d) Lesser cycles per instruction	

Code:

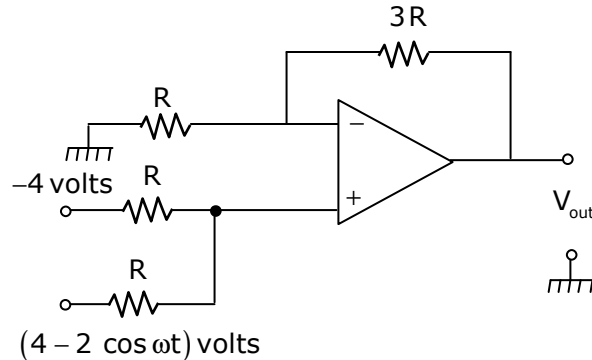|	a	b	c	d
(A)	3	2	3	1
(B)	1	2	3	3
(C)	3	3	2	1
(D)	3	3	3	1

42.
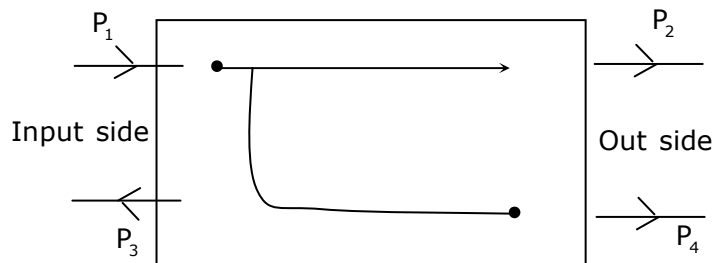
For the above directional coupler, a coupling factor in dB is:

(A) $10\log_{10}\dfrac{P_1}{P_4}$

(B) $10\log_{10}\dfrac{P_1}{P_2}$

(C) $10\log_{10}\dfrac{P_2}{P_4}$

(D) $10\log_{10}\dfrac{P_1}{P_3}$

43.

$(4-2\cos\omega t)\,\text{volts}$

The output of the above OP-amp circuit is:

(A) -0.75 volts

(B) $-2\cos\omega t\,\text{volts}$

(C) $-8\cos\omega t$ volts

(D) 16 volts

44.   A TDM system is to be designed to multiplex the following two signals:

$x_1 = 5\cos(2000\pi t)$

$x_2 = 2\cos(2000\pi t)\cos(3000\pi t)$

The minimum sampling rate is:

(A) 4kHz   (B) 5kHz   (C) 10kHz   (D) 6kHz

45.   An example of a spooled device is:

(A) A graphical display device

(B) A line printer used to print the output of a number of jobs

(C) A secondary storage device in a virtual memory system

(D) A terminal used to enter input data to a running program

46.   Which one of the following is a Dirichlet condition?

(A) $\displaystyle\int_{t_1}^{\infty}\left|x(t)\right|<\infty$

(B)   Signal x(t) must have a finite number of maxima and minima in the expansion interval

(C)   x(t) can have an infinite number of finite discontinuities in the expansion interval

(D) $x^2(t)$ must be absolutely summable

47. Consider the following instructions of 8085 microprocessor:

(1) MOV B, C

(2) STA address

(3) ORI byte

The correct sequence in the decreasing order of their respective memory space requirement is:

(A) 3, 2 and 1   (B) 1 3 and 2   (C) 1, 2 and 3   (D) 2, 3 and 1

48. A D/A converter has 5V full-scale output voltage and an accuracy of $\pm 0.2\%$. The maximum error for any output voltage will be:

(A) 5mV   (B) 10mV   (C) 20mV   (D) 1.0mV

49. If the CALL instruction of 8085 in the main program is conditional then RET instruction in the subroutine can be:

(A) Conditional

(B) Conditional or unconditional

(C) Can be determined by LDA instruction

(D) Unconditional

50.
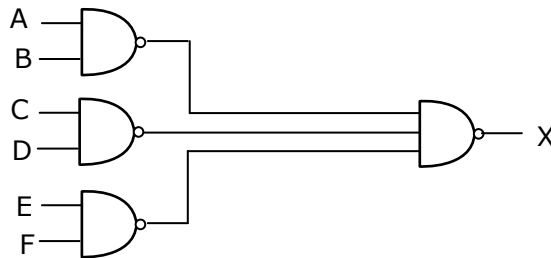
The output X of the above logic circuit is:

(A) $AB + CD + EF$ (B) $\overline{AB} + \overline{CD} + \overline{EF}$

(C) $(A+B).(C+D).(E+F)$ (D) $(\overline{A+B}).(\overline{C+D}).(\overline{E+F})$

51. The 'Double minimum' or the "Width of minimum power" method is used in microwave measurements for the measurement of:

(A) Velocity modulation   (B) Frequency distortion

(C) High VSWR   (D) Low VSWR.

52. Consider the following statements:

(1) The lock range of a PLL is the difference between the highest and lowest frequencies that the PLL can remain in lock onto.

(2) The capture range of a PLL is the range of frequencies that the voltage controlled oscillator of a PPL can produce

(3) PLL can be used to synchronize the horizontal and vertical oscillators of TV receivers to incoming sync pulses.

Which of the above statement is/are correct?

(A) 1 only   (B) 3 only   (C) 1 and 3 only   (D) 1, 2 and 3

53.
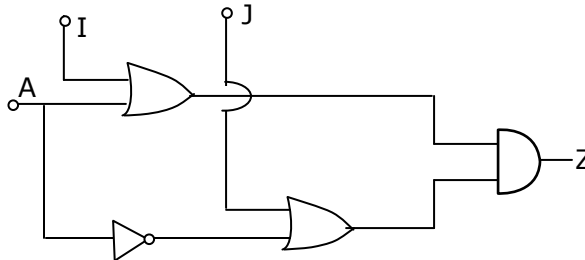
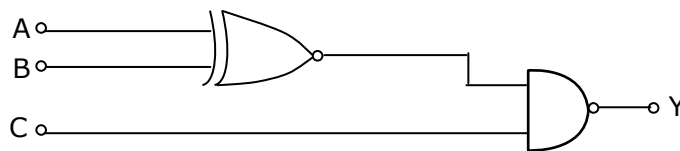The circuit shown above is to be used to implement the function

$Z = f(A, B) = \overline{A} + B.$ The value of I and J are:

(A) $I = 0$ and $J = B$ (B) $I = 1$ and $J = B$

(C) $I = B$ and $J = 1$ (D) $I = B$ and $J = 0$

54.   In the Bode plot of a unity feedback control system, the value of phase angle of $G(j\omega)$ is - 90° at the gain cross over frequency of the Bode plot, the phase margin of the system is:

(A) -180° (B) $+180^O$ (C) -90° (D) $+90^O$

55.

The Boolean expression for the output of the above logic circuit is:

(A) $Y = \overline{AB} + AB + \overline{C}$ (B) $Y = \overline{A}B + A\overline{B} + \overline{C}$

(C) $Y = A \oplus B + \overline{C}$ (D) $Y = AB + \overline{C}$

56.   Match List-I with List-II and select the correct answer using the code given below the lists:

List – I	**List – II**
(a) Cavity wave meter	(1) Impedance measurements
(b) VSWR meter	(2) Frequency measurements
(c) Bolometer	(3) Antenna measurements
(d) Fraunhofer region	(4) Microwave power measurements

Code:

|	a	b	c	d
(A)	3	1	4	2
(B)	2	1	4	3
(C)	3	4	1	2
(D)	2	4	1	3

57. The Nyquist plot of loop transfer function G(s) H(s) of a closed loop control system passes through the point (-1, j0) in the G(s) H(s) plane. The phase margin of the system is:
 (A) 0° (B) 45° (C) 90° (D) 180°

58. When compared with an RS232C serial port, the USB (Universal Serial Bus):
 (A) Supports a lower range of peripherals
 (B) Supports a faster transfer rate
 (C) Does not support 'Hot plug-ability'
 (D) Controller in PC can not detect the presence or absence of USB devices

59. A ROM is used to store the table for multiplication of two 8-bit unsigned integers. The size of the ROM required is
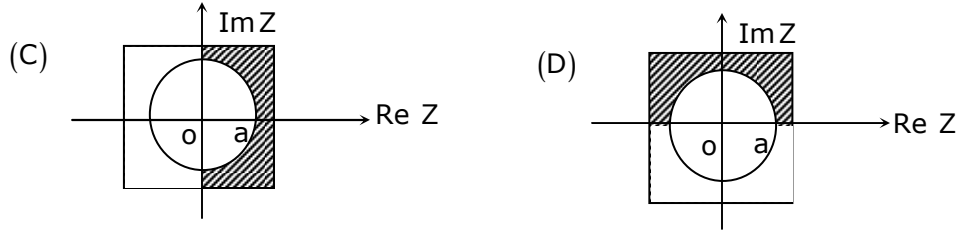 (A) 256 x 1   (B) 64K x 8   (C) 4k x 16   (D) 64K x 16

60.
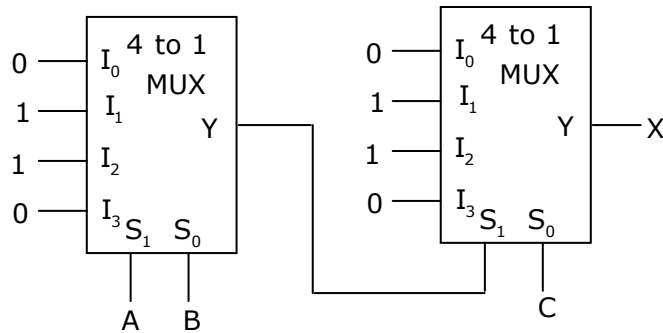
 In the above circuit, X is given by:
 (A) $X = A\overline{BC} + \overline{A}\,B\overline{C} + \overline{AB}C + ABC$ (B) $X = \overline{A}\,BC + A\,\overline{B}\,C + AB\overline{C} + \overline{ABC}$
 (C) $X = AB + BC + AC$ (D) $X = \overline{AB} + \overline{BC} + \overline{AC}$

61. Given $x(z) = \dfrac{1}{1 - az^{-1}}; |z| > |a|$ then the region of convergence is (Shaded region):
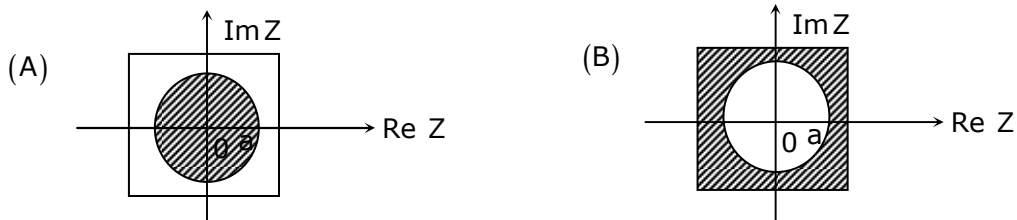
62. A 'DMA' transfer implies:

(A) Direct transfer of data between memory and accumulator

(B) Direct transfer of data between memory and I/O devices without the use of μp

(C) Transfer of data exclusively within μp registers

(D) A fast transfer of data between μp and I/O devices

63.
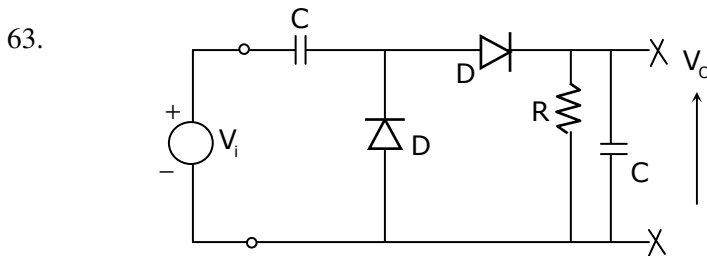
Consider the above circuit, for $V_i = V_m \sin \omega t$, the output voltage V_O for $R_L \to \infty$ will be

(A) Zero   (B) V_m (C) $2V_m$ (D) $-V_m$

64.
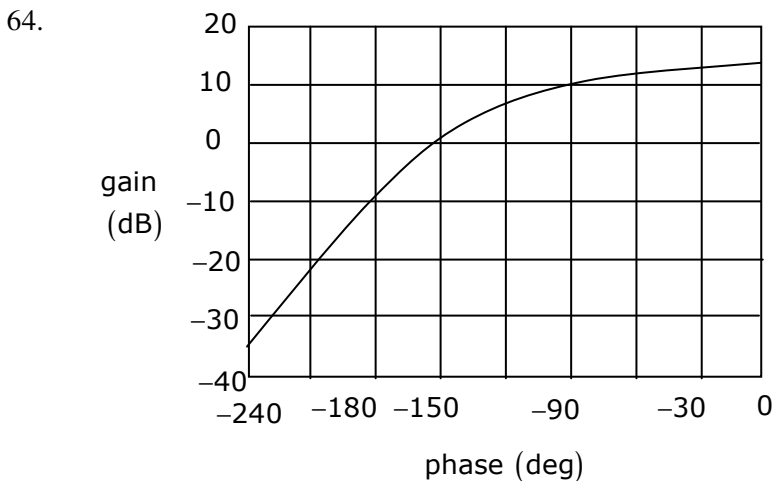
For the Nichols plot shown, the system is:

(A) Unstable   (B) Stable

(C) Overdamped   (D) Critically stable

65. The 8085 has two registers known as primary data pointers. These are registers:

(A) B and C   (B) D and E   (C) H and L   (D) C and D

66.   The correct statement is:

(A) PROM contains a programmable AND array and fixed OR array

(B) PLA contains a fixed AND array and a programmable OR array

(C) PROM contains a fixed AND array and a programmable OR array

(D) PLA contains a programmable AND array and a fixed OR array

67.
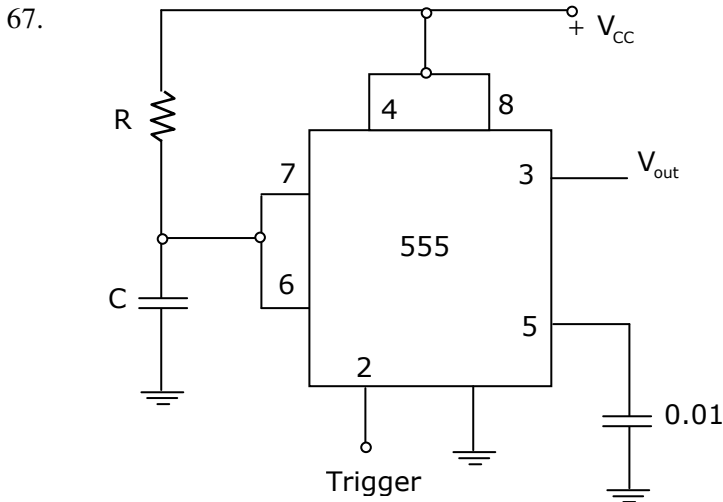
The circuit shown above is a:

(A) Bi-stable multi-vibrator   (B) Mono-stable multi-vibrator

(C) Free running multi-vibrator   (D) Ramp generator

68.   Consider the following statements:

(1) A multiplexer is analogous to a rotary switch.

(2) A decoder is a combinational logic circuit that converts binary information from 'n' input lines to a maximum of 2n distinct elements at the output.

(3) The Boolean expression for the output difference 'D' from a full subtractor is exactly the same as the output sum 'S' from a full adder.

Which of the above statements is/are correct?

(A) 2 and 4 only   (B) 4 only   (C) 1 and 3 only   (D) 1, 2 and 3

69.   A ship to ship communication system is affected by fading. A useful solution which can be used is:

(A) A more directional antenna   (B) A broadband antenna

(C) Use of frequency diversity   (D) Use of space diversity

70.   The impulse response h[n] of an LTI system is

$$h[n] = u[n+3] + u[n-2] - 2u[n-7]$$

Then the system is:

(1) Stable

(2) Causal

(3) Unstable

(4) Not casual

Which of these are correct?

(A) 1 and 2 only   (B) 2 and 3 only

(C) 3 and 4 only   (D) 1 and 4 only

71. For the experimental study of small microwave antennas, a free space environment with minimum interference by external objects, the facilities required are:

(A) RF screens, VSWR meter, waveguide twist

(B) UHF screens, slotted waveguides, power meter

(C) Anechoic chamber, Network analyzer, Pattern recorder

(D) Dark room facility, Digital recorder, Bolometer

72. READY signal in 8085 is useful when the CPU communicates with:

(A) A slow peripheral device   (B) A fast peripheral device

(C) A DMA controller chip   (D) A PPI chip

73.
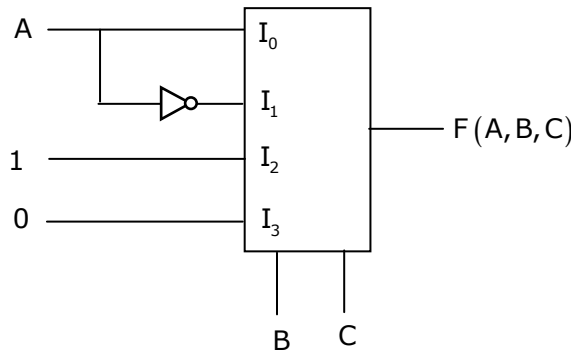
A 4 x 1 MUX is used to implement a 3-input Boolean function is as shown above. The Boolean function F(A, B, C) implemented is

(A) $F(A,B,C) = \Sigma(1,2,4,6)$ (B) $F(A,B,C) = \Sigma(1,2,6)$

(C) $F(A,B,C) = \Sigma(2,4,5,6)$ (D) $F(A,B,C) = \Sigma(1,5,6)$

74. Population inversion in semiconductor laser diode is achieved by:

(A) Lightly doping p and n sides

(B) Introducing trap centres on p and n sides

(C) Heavily doping p and n sides

(D) Reverse biasing the junction

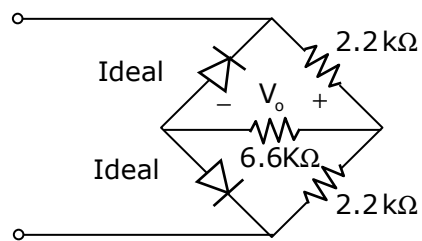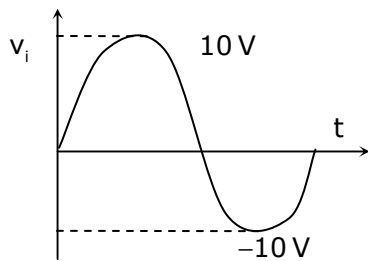75. In the following 8085 program how many times (decimal) is the DCR C executed?

> LOOP : MVI C, 78 H
>
> DCR C
>
> JNZ LOOP
>
> HLT

(A) 119   (B) 120   (C) 78   (D) 77
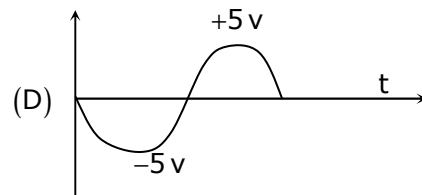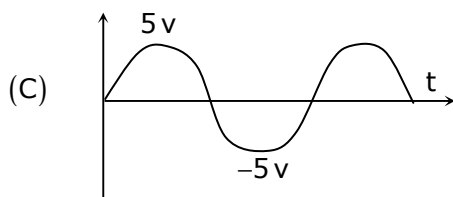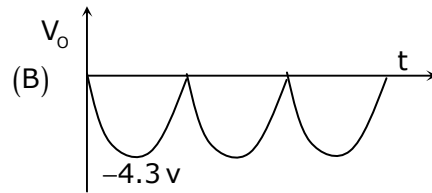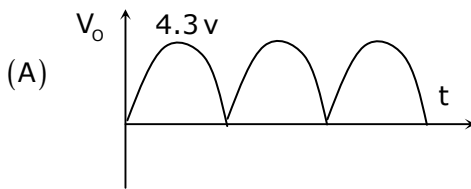
76. Processing of MOS ICs is les expensive than bipolar ICs primarily because they:

(A) Use cheaper components   (B) Need no component isolation

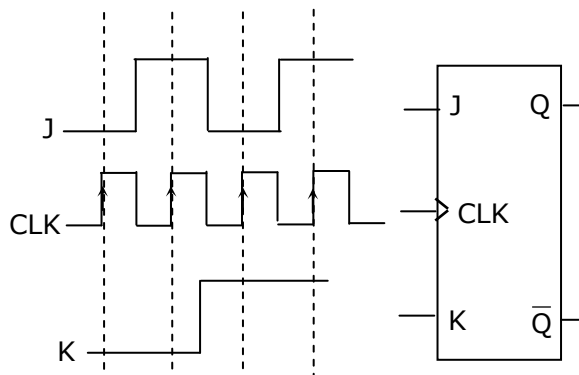(C) Require much less diffusion steps   (D) Have very high packing density

77.

The correct waveform for output (V_O) for the above network is:

78.

The J-K flip-flop shown above is initially reset, so that Q = 0. If a sequence of four clock pulses is then applied, with the J and K inputs as given in the figure, the resulting sequence of values that appear at the output Q starting with its initial state, is given by:

(A) 01011   (B) 01010   (C) 00110   (D) 00101

79.   A single instruction to clear the lower four bits of accumulator in 8085 assembly language is:

(A) XRI 0FH   (B) ANI F0H   (C) XRI F0H   (D) ANI 0FH

80.

X	Y	Q_{n+1}
0	0	1
0	1	Q_n
1	0	$\overline{Q}_n$
1	1	0

An X-Y flip-flop, whose characteristic table is given above is to be implemented using J-K flip flop. This can be done by making:

(A) $J = X, K = \overline{Y}$ (B) $J = \overline{X}, K = Y$

(C) $J = Y, K = \overline{X}$ (D) $J = \overline{Y}, K = X$
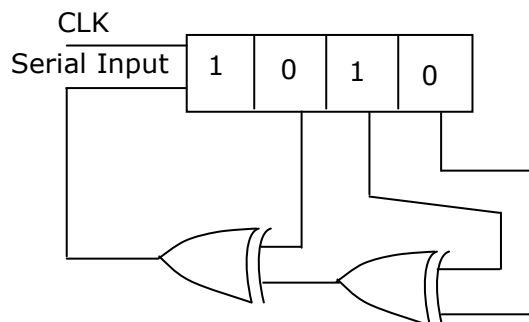
81.   The Z-transform of $-u(-n-1)$ is:

(A) $\dfrac{Z}{Z-1}$ with $|Z| > 1$ (B) $\dfrac{Z}{Z-1}$ with $0 < |Z| < 1$

(C) $\dfrac{Z}{Z-1}$ with $|Z| = 1$ (D) $\dfrac{Z}{Z-1}$ with $|Z| = 0$

82.   The interface chip used for data transmission between 8086 and a 16 bit ADC is:

(A) 8251   (B) 8253   (C) 8255   (D) 8259

83.

The shift register shown in the given figure is initially loaded with the bit pattern 1010. Subsequently the shift register is clocked, and with each clock pulse the pattern gets shifted

by one bit position to the right. With each shift, the bit at the serial input is pushed to the MSB position. After how many clock pulses will the content of the shift register become 1010 again?

(A) 3   (B) 7   (C) 11   (D) 15

84.   A microprocessor based system can perform many different functions, because:

(A) Its operation is controlled by software

(B) It is digital system

(C) It uses a RAM

(D) It can be controlled by input and output devices

85.
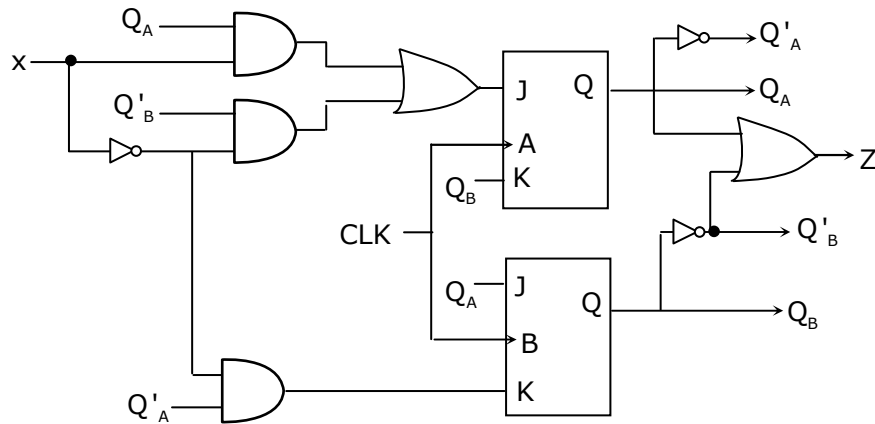
Analyze the sequential circuit shown above. Assuming that initial state is 00, determine what input sequence would lead to state 11?

(A) 1 – 1   (B) 1 – 0

(C) 0 – 0   (D) State 11 is unreachable

86.   Which of the following instructions copies a byte of data into the accumulator from the memory address given in the instruction?

(A) LDA   address   (B) LDAX   B

(C) LHLD   address   (D) STA   address

87.   The magnitude and phase of the transfer function $G(s) = \dfrac{1}{s+1}$ at $\omega = 1$ is:

(A) 0.707 and 45° (B) -3 dB and 0.78 rad

(C) 0.707 and -45° (D) 3dB and -90°

88.   IN a typical satellite communication system, which of the following could be the up-link and down-link frequencies respectively?

(A) 40 GHz and 60GHz   (B) 60 GHz and 40GHz

(C) 6 GHz and 4 GHz   (D) 4 GHz and 6 GHz

89. Which of the following is the response of a spring – mass – damper with under-damping?

(A)

(B)

(C)

(D)
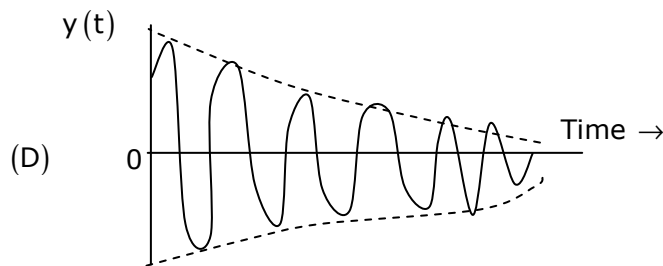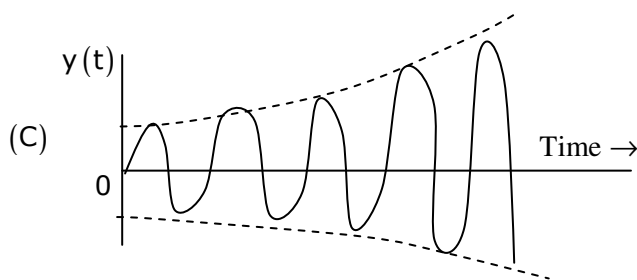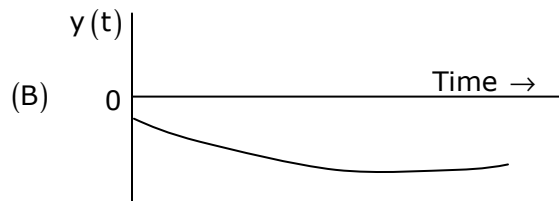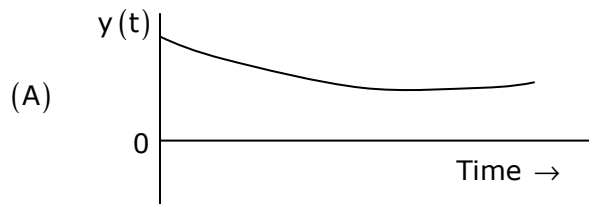
90. The output data lines of microprocessors and memories are usually tristated, because:

(A) More than one device can transmit information over the data, bus by enabling only one device at a time

(B) More than one device can transmit information over the data bus at the same time

(C) The data lines can be multiplexed for both input and output

(D) It increases the speed of data transfers over the data bus

91.
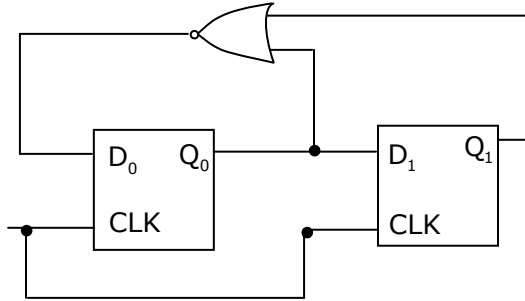
For the circuit shown, the counter state ($Q_1 Q_0$) follows the sequence:

(A) 00, 01, 10, 11, 00…..   (B) 00, 01, 10, 00, 01…..

(C) 00, 01, 11, 00, 01…..   (D) 00, 10, 11, 00, 10…..

92. From the point of view of stability and response speed of a closed loop system, the appropriate range for the value of damping ratio lies between:
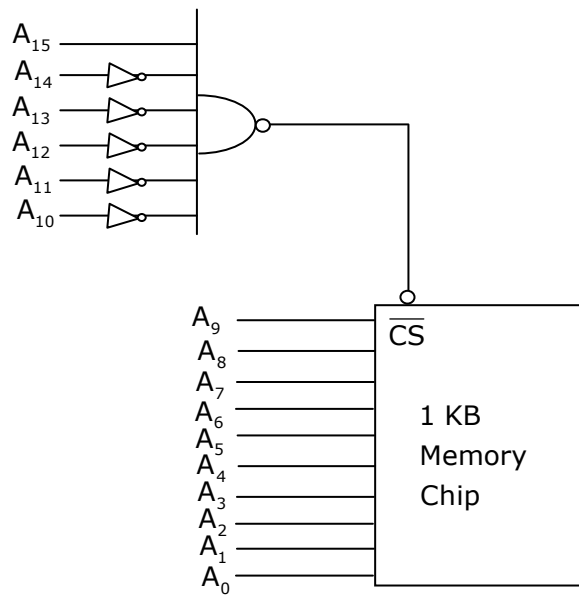
(A) 0 to 0.2   (B) 0.4 to 0.7   (C) 0.8 to 1.0   (D) 1.1 to 1.5

93.

Consider a memory chip with 1024 bytes storage connected to a 8085 chip address lines (or any microprocessor with 16 address lines) as above. What is the range of memory address?

(A) 0000 H to 03FFH   (B) 1000 H to 13FFH

(C) F000 H to F3FFH   (D) 0000 H to FFFFH

94. The output stage of a transponder onboard a satellite has a maximum power output of 10 watts. However, it is not operated at the maximum power output in order to:

(A) Conserve the available limited battery power

(B) Reduce noise due to devices

(C) Avoid intermodulation distortion

(D) Avoid heating up of the satellite beyond a pre set value

95.   The purpose of a start bit in RS232 serial communication protocol is:

(A) To synchronize receiver for receiving every byte

(B) To synchronize receiver for receiving a sequence of bytes

(C) As a parity bit

(D) To synchronize receiver for receiving the last byte

96.
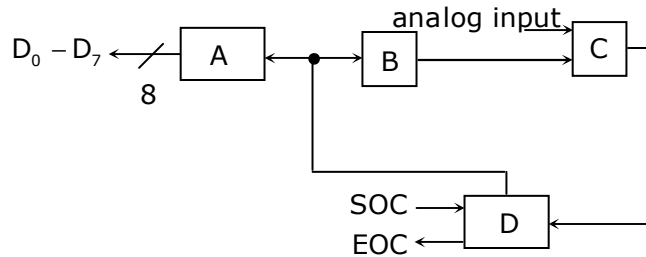
Consider the above block diagram of a successive approximation A/D converter. Match List – I with List – II and select the correct answer using the code given below the lists:

List – I (Block)	List – II (Name)
(a) A	(1) Comparator
(b) B	(2) D/A converter
(c) C	(3) Successive approximations register
(d) D	(4) Output port

Code:

	a	b	c	d
(A)	4	1	2	3
(B)	3	1	2	4
(C)	4	2	1	3
(D)	3	2	1	4

97.   The transfer function $G(s) = \dfrac{10}{(3s+1)}$ has a comer frequency at:

(A) $3 \, rad/s$ (B) $0.33 \, rad/s$ (C) $1 \, rad/s$ (D) $30 \, rad/s$

98.   A 10 bit A/D converter is used to digitize an analog signal in the 0 to 5V range. The maximum peak to peak ripple voltage that can be allowed in the DC supply voltage is, nearly:

(A) 100mV   (B) 50mV   (C) 25mV   (D) 5.0mV

99. Which one of the following represents the pole-zero location in the s-plane for lead-compensator?

(A)

(B)

(C)

(D)
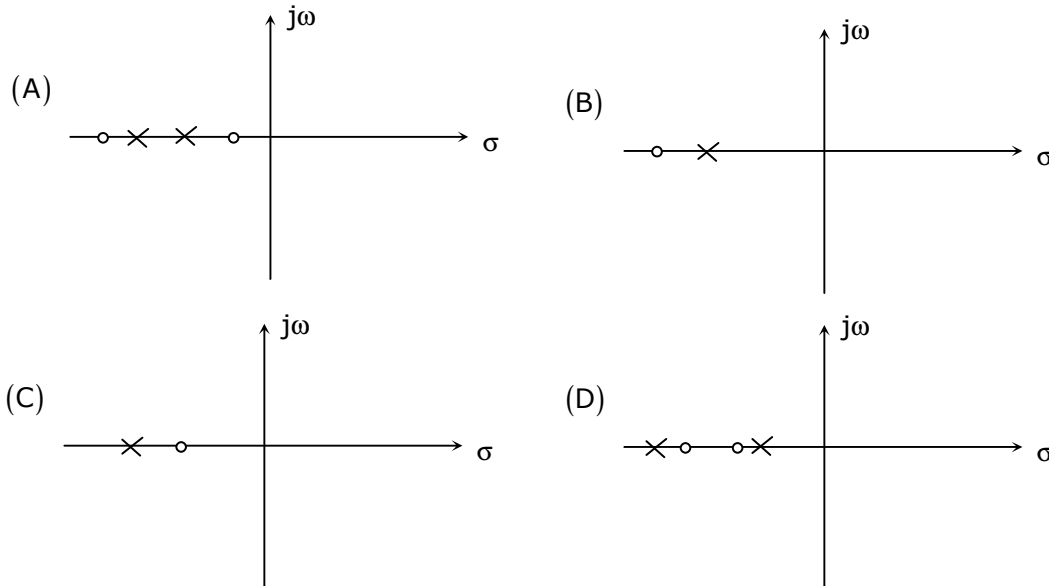
100. Consider the following applications:

(1) Wind tunnel simulation

(2) Real-time video viewing

A computer is used for:

(A) Neither 1 nor 2   (B) Both 1 and 2

(C) 1 only   (D) 2 only

Directions: -

Each of the next Twenty (20) items consists of two statements, one labeled as the 'Assertion (A)' and the other as 'Reason (R)'.

Codes:

(A) Both A and R are individually true and R is the correct explanation of A

(B) Both A and R are individually true but R is not the correct explanation of A

(C) A is true and R is false

(D) A is false and R is true

101. **Assertion (A)**   :   The TTL NAND gate in tristate output configuration can be used for a bus arrangement with more than one ate output connected to a common line.

       Reason (R)   :   The tristate configuration has a control input, which can detach a logic level (0/1) from coming onto the bus line.

102. **Assertion (A)**   :   Integral windup effect in controller causes excessive overshoot.

       Reason (R)   :   Presence of saturation in controller and actuator deteriorates the PID control.

103. **Assertion (A)** : Steady state error can be reduced by increasing integral gain.

    Reason (R) : Overshoot can be reduced by increasing derivative gain.

104. **Assertion (A)** : Source produces two symbols A and B with probability 3/4 and 1/4 respectively. For error free transmission this source should be cooled using Shannon – Fano code.

    Reason (R) : For better transmission efficiency, source and channel must be matched.

105. **Assertion (A)** : Frequency modulation and phase modulation both produce different set of frequency bands for the same modulation depth.

    Reason (R) : Frequency modulation and phase modulation and phase modulation vary the carrier phase angle or its rate.

106. **Assertion (A)** : In amplitude modulation systems the value of modulation index should be around 1.

    Reason (R) : The power carried in the intelligence carrying sidebands increases with the modulation index.

107. **Assertion (A)** : When coding signals like speech signals A-law or $\mu - $ law quantizers are used.

    Reason (R) : A-law and $\mu - $ law quantizers occupy smaller bandwidth than uniform quantizers.

108. **Assertion (A)** : PCM/FM systems transmit PCM pulses by modulating a high frequency carrier and hence occupy large band width and eliminate distortion.

    Reason (R) : Large bandwidth ensures SNR tide off and hence distortionless transmission is ensured.

109. **Assertion (A)** : It is not necessary to incorporate a very low nose amplifier in a communication satellite.

    Reason (R) : The noise temperature of the satellite antenna is usually high, since the beam is covered by the earth.

110. **Assertion (A)** : A magnetron is not an amplifier, but an oscillator producing microwaves.

    Reason (R) : In making their circular passes in the interaction space of magnetron, the electrons excite the resonant cavities into oscillations.

111. **Assertion (A)** : The slow wave structure of TWT is provided with an attenuator.

    Reason (R) : This is doe for preventing oscillations in the device.

112. **Assertion (A)**   :   Generally, magnetrons are operated in π modes.

     Reason (R)   :   Frequency for π mode can be easily separated from adjacent modes.

113. **Assertion (A)**   :   The impedance of a matched load is equal to characteristic impedance of line.

     Reason (R)   :   A matched termination absorbs the entire power incident on it.

114. **Assertion (A)**   :   In microwave point–to–point communication systems, parabolic reflector antennas are generally used.

     Reason (R)   :   A parabolic reflector antenna receiver has the property of focusing all axial rays to its focus and when used as transmitter with a feed at the focus, it will generate parallel beams along the axis with a pencil beam radiation pattern.

115. **Assertion (A)**   :   A Cassegrain antenna uses a main paraboidal reflector and a relatively small hyperboloidal sub-reflector with a small horn – feed at the vertex of the main paraboloidal reflector.

     Reason (R)   :   The optical technique developed by William Cassegrain was used in telescope design to obtain large magnification with a physically short telescope. This configuration is found to be effective in the design of microwave antenna also.

116. **Assertion (A)**   :   In microwave communication links, intensive fading at 18 GHz band occurs due to rain-drop attenuation.

     Reason (R)   :   Collective scattering from water droplets in the atmosphere will result in diminution of energy in the forward path and this is maximum at the 18 GHz band because the wavelength dimensions at these frequencies.

117. **Assertion (A)**   :   In satellite communication technique, frequency reuse effectively doubles the bandwidth and information capacity of a satellite.

     Reason (R)   :   Electromagnetic waves radiated from a transmitting antenna could be received by a distant receiving antenna by tuning the receiver in terms of frequency and polarization.

118. **Assertion (A)**   :   The main difference between a microprocessor and a microcontroller is that the former does not have any on-chip main memory whereas latter has.

     Reason (R)   :   A microprocessor does not need memory to run programs.

119. **Assertion (A)**   :   Logic analyzer offers a "delayed sweep".

     Reason (R)   :   Because the logic analyzer "sweep" is really a clock signal.

120. **Assertion (A)** : When you turn on your PC, a process called POST (power – on – self-test) begins with an electrical signal.

      Reason (R) : The electrical signal restores left over data from the chip's internal memory register.