

IES-2012- Paper-II

1. Assuming that only logic inputs X and Y are available and their complements \bar{X} and \bar{Y} are not available, the minimum number of two-input NAND gates required to implement $X \oplus Y$ would be
 (A) 2 (B) 3 (C) 4 (D) 5
2. The minimum number of NAND gates required to implement $A + A\bar{B} + A\bar{B}C$ is equal to
 (A) Zero (B) 1 (C) 4 (D) 7
3. The type of device used to interface a parallel data format with external equipment's serial format is
 (A) Key matrix (B) UART
 (C) Memory chip (D) Serial-in, parallel -out
4. A bistable multi-vibrator that functions as a voltage comparator with hysteresis is called
 (A) T flip-flop (B) D flip-flop
 (C) J-K flip-flop (D) Schmitt trigger
5. The characteristic equation of the T flip-flop is given by
 (A) $Q_{n+1} = TQ_n$ (B) $Q_{n+1} = T\bar{Q}_n + Q_n\bar{T}$
 (C) $Q_{n+1} = \bar{T}Q_n$ (D) $Q_{n+1} = T\bar{Q}_n$
6. pulse train with a frequency of 1MHz is counted using a mod-1024 ripple counter built with J-K flip-flops. For proper operation of the counter the maximum permissible propagation delay per flip-flop stage is
 (A) 100ns (B) 50 ns (C) 20 ns (D) 10ns
7. The highest speed counter is
 (A) Asynchronous counter (B) Synchronous counter
 (C) Ripple counter (D) Ring counter
8. An analog voltage of 3.41V is converted into 8-bit digital form by an A/D converter with a reference voltage of 5V. The digital output is
 (A) 1001 1001 (B) 1111 0001 (C) 1011 0111 (D) 1010 1110
9. Match list I with List II and select the correct answer using the code given below the lists:

List – I	List – II
(A) 555	(1) Microcontroller
(B) 74173	(2) Register
(C) 74163	(3) Timer
(D) 8097	(4) Counter

Code :

- A B C D
 (A) 3 4 2 1
 (B) 1 4 2 3
 (C) 3 2 4 1
 (D) 1 2 4 3

10. In which of the following type of A/D converter does the conversion time almost double for every bit added to the device?
 (A) Counter type A/D converter
 (B) Tracking type A/D converter
 (C) Single-slope integrating type A/D converter
 (D) Successive approximation type A/D converter
11. If both inputs of S-R NAND latch are low, the output will be
 (A) Unpredictable (B) Toggle
 (C) Reset (D) Remain same
12. A 10-bit DAC provides an analog output which has a maximum value of 10.23 volts. Resolution of the DAC is
 (A) 10 mV (B) 15 mV (C) 20 mV (D) 40 mV
13. Consider the following statements regarding registers and latches:
 1.Registers are temporary storage devices, whereas latches are not.
 2. A latch employs cross-coupled feedback connections.
 3. A register stores a binary word, whereas a latch does not.
 The correct statement(s) is/are
 (A) 1 only (B) 2 only (C) 1 and 3 (D) 2 and 3
14. Match List I with List II and select the correct answer using the code given below the lists:

List – I	List – II
(A) Flash converter ADC	(1) Integrating type
(B) Successive approximation ADC	(2) Fast conversion
(C) Counter ramp ADC	(3) Maximum conversion clock periods=Number of bits
(D) Dual slope ADC	(4) Uses a DAC in its feedback path

Codes :

- A B C D
 (A) 2 3 4 1
 (B) 1 3 4 2
 (C) 2 4 3 1
 (D) 1 4 3 2

15. A 12-bit ADC is operating with a $1 \mu\text{s}$ clock period and total conversion time is seen to be $14 \mu\text{s}$. The ADC must be of
(A) Flash type (B) Counting type
(C) Integrating type (D) Successive approximately type
16. The type of system which is used for determination of static error constants is determined from the number of
(A) Zeros at origin for open loop transfer function
(B) Poles at origin for open loop transfer function
(C) Zeros at origin for closed loop transfer function
(D) Poles at origin for closed loop transfer function
17. The time taken for the output to settle within $\pm 2\%$ of step input for the control system represented by $\frac{25}{s^2 + 5s + 25}$ is given by
(A) 1.2s (B) 1.6s (C) 2.0s (D) 0.4s
18. The following quantities give a measure of the transient characteristics of a control system, when subjected to unit step excitation:
1. Maximum overshoot
2. Maximum undershoot
3. Overall gain
4. Delay time
5. Rise time 6. Fall time
(A) 1, 3 and 5 (B) 2, 4 and 5 (C) 2, 4 and 6 (D) 1, 4 and 5
19. In a feedback control system, if
 $G(s) = \frac{4}{s(s+3)}$ and $H(s) = \frac{1}{s}$, then the closed – loop system will be of type
(A) 3 (B) 2 (C) 1 (D) 0
20. For a second order dynamic system, if the damping ratio is 1 then the poles are
(A) Imaginary and complex conjugate (B) In the right-half of s-plane
(C) Equal, negative and real (D) Negative and real
21. The spectral density and autocorrelation function of white noise is
(A) Delta and uniform (B) Uniform and delta
(C) Gaussian and uniform (D) Gaussian and delta
22. The average information associated with an extremely likely message is zero. What is the average information associated with an extremely unlikely message?
(A) Zero
(B) Infinity
(C) Depends on total number of messages
(D) Depends on speed of transmission of the message

23. The effect of integral controller on the steady state error (e_{ss}) and on the relative stability (R_s) of the system are
 (A) Both are increased
 (B) e_{ss} is increased but R_s is reduced
 (C) e_{ss} is reduced but R_s is increased
 (D) Both are reduced
24. A proportional integral (PI) controller results in which of the following?
 (A) Improves the transient response without affecting steady state response
 (B) Improves the steady state response without affecting transient response
 (C) Improves both transient response and steady state response
 (D) Improves the steady state response while marginally affecting transient response, for well designed control parameters
25. A liquid level controller linearly converts a displacement of 2 m to 3 m into 4–20 mA control signal. A relay serves as two position controller to open and close an inlet valve. Relay closes at 12 mA and opens at 10 mA. The hysteresis zone is
 (A) 0.1 m (B) 0.125 m (C) 0.15 m (D) 0.2 m
26. The necessary conditions for poles and zeros of the transfer function of a bridge-T network containing only resistors and capacitors and used as a compensator are
 (A) All the poles and zeros must be imaginary
 (B) Poles and zeros both can be complex
 (C) Poles can be complex but zeros must be real
 (D) Zeros can be complex but poles must be real
27. Considering the filtering property, the lead compensators and lag compensators are categorized respectively as
 (A) Low pass and high pass filters (B) High pass and low pass filters
 (C) High pass and high pass filters (D) Low pass and low pass filters
28. Match List I with List II and select the correct answer using the code given below the lists:

List – I	List – II
(P) PI control	(1) Relay controller
(Q) PD control	(2) Lead lag compensator
(R) PID control	(3) Lead compensator
(S) On-off control	(4) Lag compensator

Codes:

- | | P | Q | R | S |
|-----|---|---|---|---|
| (A) | 4 | 2 | 3 | 1 |
| (B) | 1 | 2 | 3 | 4 |
| (C) | 4 | 3 | 2 | 1 |
| (D) | 1 | 3 | 2 | 4 |

29. A phase lead compensating network has its transfer function $G_C(s) = \frac{10(1+0.04s)}{(1+0.01s)}$. The maximum phase lead occurs at a frequency of
 (A) 50rad/s (B) 25rad/s (C) 10rad/s (D) 4rad/s
30. A third order system is approximated to an equivalent second order system. The rise time of this approximated system will be
 (A) Same as the original system for any input
 (B) Smaller than the original system for any input
 (C) Larger than the original system for any input
 (D) Smaller or larger depending on the type of input
31. The constant magnitude locus for $M=1$, in G -plane is given by the following equation where $x = \text{Re}[G(j\omega)]$ and $y = \text{Im}[G(j\omega)]$
 (A) $x = -0.5$ (B) $x = 0$
 (C) $x^2 + y^2 = 0.25$ (D) $x^2 + y^2 = 1$
32. All the constant $-N$ loci in G -plane intersect the real axis in points
 (A) -1 and origin (B) -0.5 and $+0.5$
 (C) -1 and $+1$ (D) Origin and $+1$
33. For a unity feedback control system, if its open-loop transfer function is given by $G(s)$
 $H(s) = \frac{10}{(s+5)^3}$, then its gain margin will be
 (A) 20dB (B) 40dB (C) 60dB (D) 80dB
34. The gain cross-over frequency and phase margin of the transfer function $\frac{1}{s(s+1)}$ are
 (A) 1 rad/s and 45° (B) 2 rad/s and 45°
 (C) 2 rad/s and 135° (D) 1 rad/s and 135°
35. For a type 1 system, the low frequency asymptote of its Both plot will have a slope of
 (A) 0 dB/decade (B) 6 dB/decade
 (C) 20 dB/decade (D) -20 dB/decade
36. The characteristic equation of a particular system is given by $s^3 + 2s^2 + 6s + 12 = 0$. The damping ratio δ will be
 (A) $\delta = 0$ (B) $0 < \delta < 1$ (C) $\delta = 1$ (D) $\delta > 1$
37. If root loci plots of a particular control system do not intersect the imaginary axis at any point, then the gain margin of the system will be
 (A) Zero (B) 0.707 (C) 1.0 (D) Infinite

38. A system is described by the transfer function $G(s) = \frac{2s+5}{(s+5)(s+4)}$ The dc gain of the system is
 (A) 0.25 (B) 0.5 (C) 1 (D) ∞
39. The sensitivity S_K^T of transfer function $T = \frac{(1+2K)}{(3+4K)}$ with respect to the parameter K is given by
 (A) $\frac{K}{3+K^2}$ (B) $\frac{3K}{3+4K+K^2}$
 (C) $\frac{2K}{3+10K^2+8K^2}$ (D) $\frac{4K}{2+5K+7K^2}$
40. The range of K for stability of a unity feedback system whose open-loop transfer function is $G(s) = \frac{K}{s(s+1)(s+2)}$ is
 (A) $0 < K < 3$ (B) $0 < K < 6$ (C) $K > 6$ (D) $0 > K > 3$
41. The threshold effect in demodulators is
 (A) The rapid fall of output SNR when the input SNR falls below a particular value
 (B) Exhibited by all the demodulators when the input SNR is low
 (C) Exhibited by all AM suppressed carrier coherent demodulators
 (D) Exhibited by correlation receivers
42. An amplitude modulated signal is $[A + 0.5A \cos \omega_m t] \cos \omega_c t$ where ω_m and ω_c are respectively, modulating and carrier frequencies. The power efficiency is
 (A) 11.11% (B) 0.25% (C) 4.32% (D) 50%
43. The signal $x(t) = \sin(200\pi t) + 2 \sin(400\pi t)$ is modulated to produce a signal $g(t) = x(t) \sin(400\pi t)$. This is passed through a low pass filter having a cut-off frequency of 400π Hz and pass band gain of 2. Then the output signal after the filter is
 (A) 0 (B) $0.5 \sin(200\pi t)$ (C) $\sin(200\pi t)$ (D) $2 \sin(200\pi t)$
44. In communication systems, noise due to quantization error is
 (A) Linear and signal dependent
 (B) Non-linear and signal dependent
 (C) Linear and signal independent at low frequencies only
 (D) non-linear and signal dependent at low frequencies only
45. Match List I with List II and select the correct answer using the code given below the lists:

List – I	List – II
(P) Characteristics impedance	(1) $\nabla \cdot D = \rho_v$
(Q) Poynting vector	(2) $\sqrt{\frac{j\omega\mu}{(\sigma + j\omega\epsilon)}}$
(R) Displacement current	(3) $E \times H$
(S) Point form of Gauss law	(4) $\frac{\partial D}{\partial t}$

Codes:

P Q R S

- (A) 2 3 4 1
 (B) 1 3 4 2
 (C) 2 4 3 1
 (D) 1 4 3 2

46. For an earth station transmitter, with an antenna output power of 40dBW (10,000W), a back-off loss of 3dB, a total branching and feeder loss of 3dB, a total branching and feeder loss of 3dB and transmit antenna gain of 4dB, the effective isotropic radiated power (EIRP) will be
 (A) 38dBW (B) 40 dBW (C) 36 dBW (D) 47 dBW
47. A coherent binary phase-shift-keyed (BPSK) transmitter operates at a bit rate of 20Mbps. For a probability of error P_e of 10^{-4} and given carrier-to-noise (C/N) density ratio of 8.8 dB, determine energy of bit-to noise (E_b/N_0) density ratio for a receiver bandwidth equal to the minimum double-sided Nyquist bandwidth.
 (A) 23 dB (B) 2.3 dB (C) 8.8 dB (D) 0.88dB
48. An elliptically (arbitrarily) polarized wave can be broken up into
 (A) Two circularly polarized components rotating in same direction
 (B) Two circularly polarized components rotating in opposite directions
 (C) Two stationary circularly polarized components
 (D) None of these
49. Klystron operation is based on the principle of
 (A) Velocity modulation (B) Amplitude modulation
 (C) frequency modulation (D) Phase modulation
50. The following is not an application of varactor diode:
 (A) Parametric amplifier (B) Frequency tuner
 (C) Voltage controlled oscillator (D) Phase shifter
51. The following quantity is not required in the calculation of Q of a cavity resonator

- (A) Energy stored (B) Power dissipated
(C) Loss in radiation (D) Dimensions of the cavity
52. When electromagnetic waves are propagated in a waveguide
(A) They travel along the walls of the waveguide
(B) They travel through the dielectric without touching the walls
(C) They are reflected from the walls but do not travel along the walls
(D) None of these
53. Slotted line with tunable probe is not used to measure
(A) VSWR (B) Wavelength (C) Power (D) Impedance
54. In a microwave magic-T, E plane and H plane are
(A) In phase (B) Out of phase (C) Isolated (D) 90° out of phase
55. Barattors and bolometers are used for measurement of
(A) VSWR (B) Transmission losses
(C) Microwave power (D) Frequency
56. Magnetic properties of ferrites result mainly from
(A) Polarization of electromagnetic waves
(B) Dielectric behaviour of ferrite
(C) Magnetic dipole moment associated with the electron spin
(D) External magnetic fields
57. A communication link is to be set up between two stations 100km apart using $\frac{\lambda}{2}$ antenna to transmit 1kW power. The operating frequency is 100MHz and the directivity of the two antennae is 1.64. the maximum received power would be
(A) 3.06×10^{-8} W (B) 1.53×10^{-8} W
(C) 6.12×10^{-9} W (D) 1×10^{-9} W
58. In the ionosphere layer, the lowest frequency signal that penetrates the layer upon vertical incidence is given by
(A) $f_L = 81 N_{\max}$ (B) $f_L = 81 N_{\max}^2$
(C) $f_L = \sqrt{81 N_{\max}}$ (D) $f_L = 81 \sqrt{N_{\max}}$
59. An 8085 microprocessor executes the following instructions: Two numbers are represented in signed 2's complement form as
P = 11101101 and Q = 11100110
If Q is subtracted from P, the value obtained in signed 2's complement form is
(A) 10000111 (B) 00000111
(C) 11111001 (D) 011111001
60. If $(11X1Y)_8 = (12C9)_{16}$ then the values X and Y are
(A) 3 and 1 (B) 5 and 7 (C) 7 and 5 (D) 1 and 5
61. A mall code of 8085 as given below, is executed

MVI A, 7FH
ORA A
CPI A2H

The contents of the accumulator and flags after execution are

- (A) A = DD, S = 1, Z = 0, CY = 0 (B) A = 7F, S = 1, Z = 0, CY = 1
(C) A = DD, S=0, Z= 1, CY =0 (D) A = 7F, S = 0, Z = 1, CY = 1

62. Following program finds absolute value of N:

MVI A, N
ORA A
JM ONE
OUT 01H
HLT
ONE: (P)
 (Q)
OUT 01H
HLT

The instructions of (P) and (Q) must be

- (A) CMA and ADI 0H (B) CMC and ADI 0H
(C) INR A and CMC (D) INR A and CMA

63. An Intel 8085 processor is executing the program given below:

MVI A, 10H
MVI B, 10H
BACK : NOP
ADD B
RLC
JNC BACK
HLT

The number of times that the operation NOP will be executed as

- (A) 1 (B) 2 (C) 3 (D) 4

64. Match List I with List II and select the correct answer using the code given below the lists:

List I	List II
(P) Immediate	(1) LDA 30FF
(Q) Implicit addressing	(2) MOV A,B
(R) Register addressing	(3) LXI H, 2050
(S) Direct addressing	(4) RRC

Codes :

- P Q R S
(A) 3 4 2 1
(B) 1 4 2 3
(C) 3 2 4 1
(D) 1 2 4 3

65. The Boolean equation $X = [(A + \bar{B})(B + C)]B$ can be simplified to
(A) $X = \bar{A}B$ (B) $X = A\bar{B}$ (C) $X = AB$ (D) $X = \bar{A}\bar{B}$
66. For 8086 microprocessor, the jump distance in bytes for short jump range is
(A) Forward 255 and Backward 256
(B) Forward 127 and Backward 128
(C) Forward 31 and Backward 32
(D) Forward 15 and Backward 16
67. The action performed by the following instruction of 8086: MOV [1234H], AX
(A) Move contents of memory location 1234H to register AX
(B) Move the contents of register AX to memory offset 1234H
(C) Add contents at 1234h and AX
(D) Add contents of 1234h and AX and store the result in 1234H
68. While executing a program, 8085 microprocessor completes fetching of instruction JMP 2050 stored at address 2057H. The contents of the program counter after fetching the instruction would be
(A) 2050H (B) 2057 H (C) 205A H (D) 2051 H
69. For Opcode fetch operation in 8085 microprocessor
(A) $S_1 = 0, S_2 = \overline{1RD} = 0, ALE$ high in T_1
(B) $S_1 = 1, S_2 = \overline{1RD} = 0, ALE$ high in T_1
(C) $S_1 = 1, S_2 = \overline{1RD} = 0, ALE$ high in T_2
(D) $S_1 = 1, S_2 = \overline{1RD} = 0, ALE$ high in T_2
70. In a microprocessor, the service routine for a certain interrupt starts from a fixed location of memory which cannot be externally set, but the interrupt can be delayed or rejected. Such an interrupt is
(A) Non-maskable and non-vectorized
(B) Maskable and non-vectorized
(C) Non-maskable and vectorized
(D) Maskable and vectorized
71. A memory system has to total of 8 memory chips, each with 12 address lines and 4 data lines. The size of the memory system is
(A) 16k bytes (B) 32k bytes (C) 48 k bytes (D) 64k bytes
72. Consider the following statements for a DRAM:
1. Bit is stored as a charge.
2. It is made of MOS transistors.
3. Speed of DRAM is faster than processors.
4. Each memory cell requires six transistors
Which of these statements are correct
(A) 1 and 2 only (B) 2 and 3 only
(C) 3 and 4 only (D) 1,2,3 and 4

73. The access time of a cache memory is 100ns and that of main memory is 1 ms. 80% of the memory requests are for read and others are for write. Hit ratio for read only accesses is 0.9. A write through procedure is used. The average access time of the system for both read and write requests is
(A) 200 ns (B) 360 ns (C) 720 ns (D) 1100ns
74. A virtual memory system has an address space of 8k words, a memory space of 4k words and page and block sizes of 1k words. The number of page faults using of 1k words. The number of page faults using LRU policy, for following page references is 10246215700
(A) 5 (B) 7 (C) 9 (D) 10
75. Among memory types, the abbreviation MPDRAM stands for
(A) Multi port Dynamic Random Access Memory
(B) Multipoint Dynamic Random Access Memory
(C) Multipoint Disk Random Access memory
(D) Multi port Dimensional Random Access memory
76. The following register holds the instruction before it goes to the decoder:
(A) Control register (B) Accumulator
(C) Address register (D) Data register
77. The incorrect match (when $n > 1$) is
(A) SISD Model of computer : 1 control unit and 1 ALU
(B) SIMD Model of computer : 1 control unit and n ALUs
(C) MISD Model of computer : n control units and n ALUs
(D) MIMD model of computer : n control units and 1 ALU
78. In case the code is position dependent , the most suitable addressing mode is
(A) Direct mode (B) Indirect mode
(C) Relative mode (D) Indexed mode
79. The data structure needed to convert infix notation to prefix notation is
(A) Queue (B) Stack (C) Tree (D) Graph
80. A binary tree T has n leaf nodes. The number of nodes of degree 2 in T is
(A) $\log_2 n$ (B) $n - 1$ (C) n (D) 2^n
81. For the 8085 assembly language program given below, the content of the accumulator after the execution of the program is
3000 MVI A, 45H
3002 MOV B, A
3003 STC
3004 CMC
3005 RAR
3006 XRA B
3007 HLT
(A) 00 H (B) 45 H (C) 67 H (D) E7 H

82. The 8254 Programmable Interval Timer is set to work in MODE 5. The following would best describe its function:
(A) Software triggered strobe (B) Hardware triggered strobe
(C) Square wave generator (D) Interrupt on terminal count
83. To configure port A and port B as output ports, port C not being used in the 8155 programmable interface, the control word should have the value
(A) 03H (B) 01H (C) 02H (D) 60H
84. The 8085 microprocessor instructions with required number of T states are given below. Which pair is correctly matched?
(A) STAX: 8 T-states (B) SPHL : 6 T-states
(C) SIM : 7 T-states (D) STA : 12 T-states
85. The crystal frequency of 8085 microprocessor is 6 MHz. The time required to execute instruction XTHL over this microprocessor is
(A) 5.33 μ sec (B) 10.67 μ sec (C) 4.33 μ sec (D) 8.67 μ sec
86. In 8085 microprocessor, after the execution of RST 5 instruction, the program control shifts to
(A) 0030 H (B) 0005 H (C) 0028 H (D) 0024 H
87. Consider the following 8085 interrupts:
1. TRAP 2. INTR
3. RST 6 4. RST 7.5
5. RST 0
The software interrupts are
(A) 1 and 3 only (B) 2 and 5 only
(C) 3 and 5 only (D) 1,2,3,4 and 5

Directions: Each of the next thirteen (13) items consists of two statements, one labeled as the 'Statement (I)' and the other as 'Statement (II)'. You are to examine these two statements carefully and select the answers to these items using the codes given below:

Codes:

- (A) Both Statement (I) and Statement (II) are individually true and Statement (II) is the correct explanation of Statement (I)
(B) Both Statement (I) and Statement (II) are individually true but Statement (II) is *not* the correct explanation of Statement (I)
(C) Statement (I) is true but Statement (II) is false
(D) Statement (I) is false but Statement (II) is true
88. **Statement (I)** : In a transistor designed to be used for power amplification, the collector size is largest relative to the emitter and base.
Statement (II) : The collector is connected to the body of the transistor and hence to a heat sink for heat dissipation to be effective.
89. **Statement (I)** : The carry look-ahead adder is a fast adder
Statement (II) : The carry look-ahead adder generates the carry and the sum digits directly.

90. **Statement (I)** : Root loci are symmetrical with respect to real axis of the s-plane.
Statement (II) : Root loci are normally symmetrical with respect to the perpendicular axis of symmetry of the pole-zero combination of the loop transfer function.
91. **Statement (I)** : Nyquist criterion is a powerful tool to determine stability of a closed loop system using open loop transfer function.
Statement (II) : Nyquist criterion relates the locations of poles and zeros of the closed loop transfer function.
92. **Statement (I)** : A second order system subjected to a unit impulse oscillates at its natural frequency.
Statement (II) : Impulse input contains frequencies from $-\infty$ to $+\infty$.
93. **Statement (I)** : All the systems which exhibit overshoot in transient response will also exhibit resonance peak in frequency response.
Statement (II) : A large resonance peak in frequency response corresponds to a large overshoot in transient response.
94. **Statement (I)** : In a prototype second order system the rise time t_r and bandwidth are inversely proportional.
Statement (II) : Increasing $n \omega_n$ increases bandwidth while t_r reduces.
95. **Statement (I)** : The phase angle plot in Bode diagram is not affected by the variation in open loop gain of the system.
Statement (II) : The variation in gain of the system has no effect on the phase margin.
96. **Statement (I)** : FM spectrum consists of a carrier and an infinite number of side band components.
Statement (II) : FM signals are used only in TV transmission to ensure better picture quality.
97. **Statement (I)** : In the two body system consisting of the earth and a satellite, centre of mass always coincides with centre of the earth.
Statement (II) : Mass of earth is much greater than mass of satellite.
98. **Statement (I)** : Gunn effect device is a slice made from n-doped GaAs and provides negative resistance characteristics.
Statement (II) : GaAs has an empty energy band higher in energy than the filled or partly filled bands.
99. **Statement (I)** : Power output of the lowest mode in a reflex klystron is maximum.
Statement (II) : Lower modes occur at higher repeller voltages where the acceleration of bunched electrons on return is maximum.
100. **Statement (I)** : The DMA technique is more efficient than the Interrupt-driven technique for high volume I/O data transfer.
Statement (II) : The DMA technique does not make use of the Interrupt mechanism.
101. In locations where the humidity is low, ICs based on one of the following technologies should be handled only after grounding the body. The technology is
(A) TTL (B) CMOS (C) DTL (D) I2L

102. If a feedback control system has its open-loop transfer function

$$G(s)H(s) = \frac{K}{[s(s+2)(s^2+2s+5)]}$$

the coordinates of the centroid of the asymptotes of its root-locus are

- (A) -1 and 0 (B) 1 and 0 (C) 0 and -1 (D) 0 and 1

103. Match List I with List II and select the correct answer using the code given below the lists:

List I	List II
(P) HTL	(1) High fan-out
(Q) COMOS	(2) Highest speed of operation
(R) I ² L	(3) Highest noise immunity
(S) ECL	(4) Lowest product power and delay

Code:

- | | | | | |
|-----|---|---|---|---|
| | P | Q | R | S |
| (A) | 3 | 4 | 1 | 2 |
| (B) | 2 | 4 | 1 | 3 |
| (C) | 3 | 1 | 4 | 2 |
| (D) | 2 | 1 | 4 | 3 |

104. The figure of merit of a logic family is given by

- (A) Gain bandwidth product
 (B) (Propagation delay time) × (Power dissipation)
 (C) (Fan-out) × (Propagation delay time)
 (D) (Noise –margin) × (Power dissipation)

105. The correct expression is

- (A) $\bar{A}B + A\bar{B} = \bar{A}B(A + B)$ (B) $\bar{A}B + \bar{A}B = AB(\bar{A} + \bar{B})$
 (C) $\bar{A}B + A\bar{B} = AB(\bar{A} + \bar{B})$ (D) $\bar{A}B + \bar{A}B = \bar{A}B(A + 1)$

106. Binary data is being represented in size of byte and in 2's complement form. The number of 0's present in representation of (-127)DECIMAL IS

- (A) 8 (B) 7 (C) 6 (D) 5

107. Simplified form of the logic expression

- $(A + \bar{B} + C)(A + \bar{B} + \bar{C})(A + B + C)$ is
 (A) $\bar{A}B + \bar{C}$ (B) $A + \bar{B}C$ (C) A (D) $AB + \bar{C}$

108. For a transistor used as a switch, t_d is delay time, t_r is rise time, t_s is storage time and t_f is fall time. Then turn-on time t_{ON} and turn-off time t_{OFF} are respectively

- (A) $(t_d + t_s)$ and $(t_r + t_f)$ (B) $(t_d + t_f)$ and $(t_s + t_r)$
 (C) $(t_r + t_s)$ and $(t_d + t_f)$ (D) $(t_d + t_r)$ and $(t_s + t_f)$

109. In the circuit shown, the need of the resistor R_F is
 (a) To increase the overall gain
 (b) To stabilize the circuit
 (c) To increase input impedance
 (d) To prevent saturation
110. A 40V dc supply is connected across the network comprising of zener and silicon diode as shown. The regulated voltages V_{01} , V_{02} and source current I_s are
 (A) 2.4V, 5.1 V and 21.7mA (B) 3V, 6V and 22.7mA
 (C) 3.3V, 9.3V and 20.5mA (D) 4V, 10V and 20mA
111. For a full wave rectifier, with sinusoidal input and inductor as filter, ripple factor for maximum load current and minimum load current conditions are respectively
 (A) 0.1 and 1 (B) 0.1 and 0.47
 (C) 0 and 0.47 (D) 0 and 0.22
112. The second-harmonic component in the output of a transistor amplifier, without feedback, is B_2 . The second harmonic component, with negative feedback B_2 is equal to (where A = Amplifier gain and β = feedback factor)
 (A) $\frac{B_2}{1 + A\beta}$ (B) $B_2(1 + A\beta)$ (C) $\frac{B_2}{\beta}$ (D) $\frac{B_2}{A\beta}$
113. The lower 3dB frequency of an n-stage amplifier with non-interacting stages is given by
 (A) $\frac{f_L}{\sqrt{\frac{1}{2^n} - 1}}$ (B) $f_L \left[\sqrt{\frac{1}{2^n} - 1} \right]$ (C) $\frac{f_L}{\sqrt{\frac{1}{2^n} - n}}$ (D) $f_L \left[\sqrt{\frac{1}{2^n} - n} \right]$
114. An output signal of a power amplifier has amplitudes of 2.5V fundamental, 0.25V second harmonic and 0.1V third harmonics. The total percentage harmonic distortion of the signal is
 (A) 12.8% (B) 10.8% (C) 6.4% (D) 1.4%
115. A change in the value of the emitter resistance R_e in a differential amplifier
 (A) Affects the difference mode gain, A_d
 (B) Affects the common mode gain, A_c
 (C) Affects both A_d and A_c
 (D) Does not affect either A_d or A_c
116. In a transistor biased in the active region, thermal runaway is due to
 (A) Base emitter voltage V_{BE} which decreases with rise in temperature
 (B) Change in reverse collector saturation current due to rise in temperature
 (C) Heating the transistor
 (D) Changes in β which increases with temperature
117. A transistor is said to be useful to be configured as an amplifier when its β is
 (A) less than 0 (B) Between 0 and 1
 (C) Between 1 and 50 (D) > 50

118. A BJT is biased with a power supply of 12V. For minimum heat dissipation, the drop across the transistor will be
(A) 6V (B) 9V (C) 12V (D) > 9V but < 12V
119. A bipolar junction transistor with forward current transfer ratio $\alpha = 0.98$, when working in CE mode, provides current transfer ratio β as
(A) 98 (B) 0.02 (C) 49 (D) 0.49
120. Match List I with List II and select the correct answer using the code given below the lists:

List - I	List - II
(P) Tuned circuits	(1) Schottky diode
(Q) Voltage reference	(2) Varactor diode
(R) High frequency switch	(3) PIN diode
(S) Current controlled attenuator	(4) Zener diode

Code :

- P Q R S
- (A) 2 4 1 3
- (B) 3 4 1 2
- (C) 2 1 4 3
- (D) 3 1 4 2